

Netburner Module/Dev Board Cross Ref for MOD5270B/MOD-DEV-70

Date 3/30/2013 © Samudra Haque N3RDX

MOD-DEV-70 Connector J2_C					MOD5270B
ODD	EVEN	Used	Pin Name	Connections	Fn1/Fn2/Fn3/GPIO
1		Y	GND		GND
	2	Y	VCC3V		VCC3V I/P
3		Y	URXD0	JP2/2	UART0_RX///PUARTL0
	4	Y	UTXD0	U9/12; U7/25	UART0_TX///PUARTL1
5			J2[5]		NC
	6	Y	J2[6]	S2/2	D14///PDATAH14
7		Y	J2[7]	S2/3	D13///PDATAH13
	8	Y	J2[8]	S2/1	D15///PDATAH15
9		Y	J2[9]	S2/5	D11///PDATAH11
	10	Y	J2[10]	S2/4	D12///PDATAH12
11		Y	J2[11]	S2/6	D10///PDATAH10
	12	Y	J2[12]	S2/7	D9///PDATAH9
13		Y	J2[13]	S2/8	D8///PDATAH8
	14		J2[14]		GND
15		Y	J2[15]	R7	D0///PDATAL0
	16	Y	J2[16]	R8	D1///PDATAL1
17		Y	J2[17]	R11	D4///PDATAL4
	18	Y	J2[18]	R9	D2///PDATAL2
19		Y	J2[19]	R12	D5///PDATAL5
	20	Y	J2[20]	R13	D6///PDATAL6
21		Y	URXD1	U9/19	UART1_RX///PUARTL4
	22	Y	UTXD1	U9/14	UART1_TX///PUARTL5
23		Y	J2[23]	R10	D3///PDATAL3
	24	Y	J2[24]	R14	D7///PDATAL7
25		Y	QSPI-CLK	J5/5	SPI_CLK/I2C_SCL//PQSPI2
	26		J2[26]		T3OUT/UART_RTS-/SPI_CS3-/PTIMER6
27		Y	QSPI-DIN	J5/7	SPI_DIN/I2C_SDA//PQSPI1
	28	Y	QSPI-DOUT	J5/2	SPI_DOUT///PSQPI0
29		Y	J2[29]	JP3/2	UART0_CTS-///PUARTL3
	30		J2[30]		SPI_CS0-/PSQPI3
31			J2[31]		T0IN/DREQ0-/PTIMER1
	32	Y	J2[32]	U9/13	UART1_RTS-/U2_RTS-//PUARTL6
33		Y	J2[33]	U9/18	UART1_CTS-/U2_CTS-//PUARTL7
	34		J2[34]		T1OUT/DACK1//PTIMER2
35		Y	J2[35]	J5/1	T2IN/DREQ2-/T2OUT/PTIMER5
	36		J2[36]		T0OUT/DACK0//PTIMER0
37			J2[37]		T1IN/DREQ1-/T1OUT/PTIMER3
	38		J2[38]		UART0_RTS-///PUARTL2
39		Y	J2[39]	R27	I2C_SDA///PFEC12C1
	40	Y	J2[40]	JP4/1	SPI_CS1-/SD_CKE//PQSPI4
41			J2[41]		UART2_RX///PUARTH0
	42	Y	J2[42]	R26	I2C_SCL///PFEC12C0
43		Y	IRQ1-	U2/3	IRQ1-///PIRQ1
	44		J2[44]		UART2_TX///PUARTH1
45		Y	IRQ3-	MOD5270B/R	IRQ3-///PIRQ3
	46	Y	GND		GND
47		Y	IRQ5-	MOD5270B/R	IRQ5-///PIRQ5
	48		IRQ7-		IRQ7-///PIRQ7
49		Y	GND		GND
	50	Y	VCC3V		VCC3V I/P

Source

MOD-DEV-70-SCH-R1p7.pdf Rev 8/6/2009

MOD5270 Ethernet Core Module Data Sheet Rev August 31, 2012

Legend

Unallocated  
Invalid combination